

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/624,716  
Filing Date ..... July 21, 2003  
Inventor..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2812  
Examiner ..... Geyer, Scott B.  
Attorney's Docket No. .... MI22-2357  
Title: ..... Methods of Forming Semiconductor Constructions

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO/SB/08a

The attached form PTO/SB/08a is submitted in compliance with 37 CFR §1.56.  
Copies of the cited art are included with the exception of U.S. patents and published U.S.  
applications (1276 OG 55). No admission is made regarding whether all the submitted  
references are prior art.

Respectfully submitted,

Dated: August 7, 2006

By: Jennifer J. Taylor  
Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711